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Method for Fabricating a Vertical Bipolar Junction Transistor

Appl. No. : 10/707,260 Confirmation No. 1259
Applicant : Jing-Horng Gau,
Anchor Chen
Filed : December 2, 2003
TC/A.U. : 2825
Examiner : Lee, Calvin
Docket No. : NAUP0535USA0
Customer No. : 27765

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

AMENDMENT

Sir:

- 5 In response to the Office action of October 19, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

- 10 **Amendments to the Drawings** begin on page 6 of this paper and include both an attached replacement sheet and an annotated sheet showing changes:

Remarks/Arguments begin on page 7 of this paper.

An Appendix including amended drawing figures is attached following page 10 of this paper.